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**G06F 9/46****G06F 9/46****G06F 13/14****G06F 13/24**(21) Application number: **02281381**(71) Applicant: **FUJITSU LTD**

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(72) Inventor: **UEHARA YOSHIYUKI**(54) **INTERRUPTION MASK SYSTEM FOR VIRTUAL COMPUTER**

overhead is decreased.

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(57) Abstract:

**PURPOSE:** To reduce the load of a VM monitor, and decrease an overhead by providing the interruption mask registers of an I/O bus adapter at every VM domain, and using the mask register common to the entire VM domains by a test program.

**CONSTITUTION:** Mask registers 3-0 - 3-3 assigned to each VM domain mask an interruption request raised from an I - O device by the virtual logical number of the I - O device identified by an OS in the VM domain, and transmit the permitted interruption request through an extended control register 4 to a host device. A mask register 5 common to the entire VM domains masks the interruption request passing through entire I/O bus adapters 2-0 - 2-3 by the physical number of the I/O device, and transmits it to the host device. The mask register HIM 5 can be used by the test program of the I/O at the time of performing the IPL of the test program, in a virtual computer mode. Thus, the load of the VM monitor at the time of the interruption mask control of each virtual computer is reduced, and the

